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**Dr. Ahmed Sajjad Khan**

In recognition of the publication of the paper entitled  
**Design and Implementation of Parallel Self-Timed Adder Using VHDL**

Published In IJNRD ( [www.ijnrd.org](http://www.ijnrd.org) ) ISSN Approved & 8.76 Impact Factor

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Co-Authors - Mohammad Hassan, Syed  
Aiman, Shreya Raut, Shefali Gedam



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