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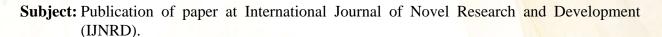
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To.

Padala Jayakrishna Chary

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Dear Author,

With Greetings we are informing you that your paper has been successfully published in the International Journal of Novel Research and Development (ISSN: 2456-4184). Following are the details regarding the published paper.

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Title of Paper : Design of 16-bit Adder, Multiplier and ALU using Reversible Logic Gates

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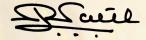
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