© 2022 IJNRD | Volume 7, Issue 6 June 2022 | ISSN: 2456-4184 | IJNRD.ORG

IJNRD.ORG ISSN : 2456-4184

INTERNATIONAL JOURNAL OF NOVEL RESEARCH AND DEVELOPMENT (IJNRD) | IJNRD.ORG

An International Open Access, Peer-reviewed, Refereed Journal

VOLTAGE CONTROL IN A POWER SYSTEM WITH RENEWABLE SOURCES OF ENERGY

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ABSTRACT

Voltage sags result in unwanted operation stops and large economic losses in industrial applications. A dynamic voltage restorer (DVR) is a power-electronics-based device conceived to protect high-power installations against these events. However, the design of a DVR control system is not straightforward and it has some peculiarities. First of all, a DVR includes a resonant (LC) connection filter with a lightly damped resonance. Secondly, the control system of a DVR should work properly regardless of the type of load, which can be linear or non-linear, to be protected. In order to improve the utilization rate and power quality of distributed new energy power generation technology and, to solve the voltage fluctuation problem in the operation of the distributed photovoltaic storage and grid-connected system. This project proposes a control strategy based on DVR (dynamic voltage restorer) for operation of distributed photovoltaic storage and grid-connected. The remaining photovoltaic output energy is stored in energy storage via active bridge to reduce the waste of photovoltaic power.

To compensate the output voltage fluctuation of photovoltaic grid-connected inverter, the DVR was connected to the energy storage. And PI controller parameters of the DVR are optimized by ANFIS algorithm; realize the recovery of output voltage fluctuation of the photovoltaic grid-connected inverter. The advantages of the proposed control strategy are demonstrated using simulations, and the results show that the proposed strategy can ensure the quality of PV output voltage in the photovoltaic storage and grid connected. PV based DVR system is comprised of PV System with low and high power DC-DC boost converter, PWM voltage source inverter, series injection transformer and semiconductor switches. Simulation results proved the capability of the proposed DVR in mitigating the voltage sag, swell and outage in a low voltage distribution system.

INTRODUCTION

Most downtimes in industry are due to voltage sags. Unfortunately, it is difficult to immunize equipment against these voltage events and, if the sag lasts for a long time, equipment shutdown is inevitable. Uninterruptible power supplies (UPSs) are often used for protecting sensitive loads against voltage sags. UPSs are widely applied to protect low-power loads such as computers or small electronic loads. They replace the grid when a voltage sag takes place and, when the voltage level recovers, loads are gently reconnected to the grid. However, a UPS has to deliver all the power consumed by the protected loads during a sag. This means that a UPS requires large batteries to protect loads against long-duration voltage sags and, consequently, its application is greatly restricted by the size and cost of batteries. A dynamic voltage restorer (DVR) is conceived to protect sensitive loads. against voltage sags and swells.

This device is connected in series with an electrical distribution line and, typically, it consists of a voltage source converter (VSC), a DC capacitor, a coupling transformer, batteries, and an AC filter. When voltage sag takes place, a DVR injects the required voltage in series with the feeding line and the load voltage remains unchanged. The main advantage of DVRs is that only a portion of the power consumed by the load is supplied from the batteries. This means that batteries can be made much smaller than in a typical UPS and cost can be reduced. These reductions in battery size and cost make DVRs very attractive for high-power applications where a UPS may be infeasible. A series-connected power-electronics device that was able to restore the voltage of a load under distorted grid conditions. AC–DC converter was used to maintain the DC voltage constant so that no additional energy storage elements were required. The main task of a DVR is to control the load voltage. Therefore, a control scheme is commonly adopted. DVRs are sometimes controlled by using open-loop techniques. Stability is guaranteed with this control technique if the plant is stable (always the case for a DVR).

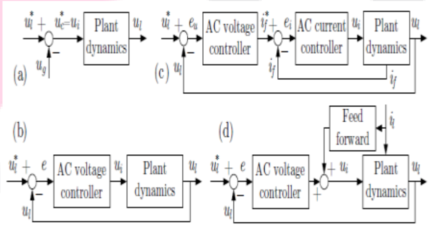


Fig1.1 Most relevant dynamic voltage restorer (DVR) control strategies: (**a**) open-loop control, (**b**) single-loop control, (**c**) multi-loop control, and (**d**) single-loop control with current feed-forward.

However, the system performance deteriorates when there are disturbances. Open-loop control has clear drawbacks:

- Accurate reference tracking is only possible if the plant model is exactly known.
- Disturbances cannot be rejected.
- It is almost impossible to track voltage harmonics.

CONTROL TECHNIQUES

Open-loop control techniques were applied to control a DVR, obtaining a fast transient response. However, performance was poor if the AC filter included a capacitor because of the LC filter resonance. In most cases, DVRs are controlled by using a feedback control scheme. Additionally, the current consumed by the sensitive load can be added as a feed-forward signal. Feedback control provides accurate reference tracking provided the closed-loop plant is stable.

However, DVR feedback control can be difficult because

(a) the load modifies the plant dynamics and

(b) the LC filter resonance is difficult to damp with a controller based on a single loop.

If the controller is applied in natural magnitudes or in a stationary RF (*ab*), decoupling equations are not required. However, a resonant controller is needed to achieve zero steady-state error for the fundamental component. By far, the most common alternative is to use an SRF because the fundamental components of all magnitudes are constant values in steady state. Therefore, a proportional-integral (PI) controller is enough to track balanced voltage sags, although the dq-axis dynamics are coupled.

In addition, a phase-locked loop (PLL) is needed to synchronize with the grid voltage. An alternative controller was implemented by using time-varying phasors that did not require a PLL and made it possible to independently control each phase. However, the transient response was slower when compared to other control algorithms because the phasors needed to be estimated. The simplest solution to damp the resonance is to add a resistor close to the AC capacitor, but this increases losses. A multi-loop control scheme like the one depicted in Figure 1c is a classical solution to damp resonances: first, the current through the filter inductor (il) is controlled by the inner AC-current controller, and, secondly, the load voltage (ul) is controlled by the AC-voltage controller. With this control scheme, the resonance can be actively damped and no extra passive elements are required.

Nevertheless, extra measurements are required. A DVR can also be controlled by using a single control loop. For instance controlled a DVR with an LC filter by applying a PID controller and a fast transient response with a reduced overshoot was obtained. However, the load voltage quality deteriorated when loads were non-linear. Alternatively, Roncero-Sánchez et al. damped the resonance by applying a PI controller plus a notch filter tuned at the resonant frequency: the notch filter simplified the PI controller design, but the result was not robust against variations in the system parameters.

POWER QUALITY PROBLEMS

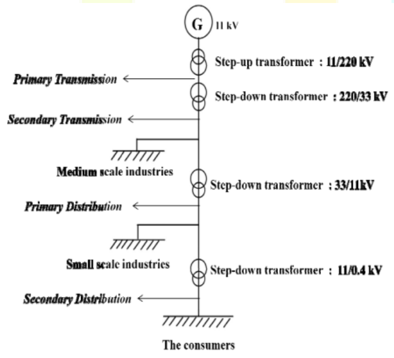


Fig. Single line diagram of power

Power distribution systems, ideally, should provide their customers with an uninterrupted flow of energy at smooth sinusoidal voltage at the contracted magnitude level and frequency. However, in practice, power systems, especially the nonlinear loads, which significantly affect the quality of power supplies. As a result of the nonlinear loads, the purity of the waveform of supplies is lost. This ends up produce power quality problems. While power disturbances occur on all electrical systems, the sensitivity of today's sophisticated electronic devices makes them more susceptible to the quality of power supply. For some sensitive devices, a momentary disturbance can cause scrambled data, interrupted communications, a frozen mouse, system crashes and equipment failure etc. A power voltage spike can damage valuable components. Power Quality problems encompass a wide range of disturbances such as voltage sags/swells, flicker, harmonics distortion, impulse transient, and
interruptions.

- Voltage dip: A voltage dip is used to refer to short-term reduction in voltage of less than half a second.
- Voltage sag: Voltage sags can occur at any instant of time, with amplitudes ranging from
  10 – 90% and a duration lasting for half a cycle to one minute.
- Voltage swell: Voltage swell is defined as an increase in rms voltage or current at the power frequency for durations from 0.5 cycles to 1 min.
- Voltage 'spikes', 'impulses' or 'surges': These are terms used to describe abrupt, very brief increases in voltage value.
- Voltage transients: They are temporary, undesirable voltages that appear on the power supply line. Transients are high over-voltage disturbances (up to 20KV) that last for a very short time.
- Harmonics: The fundamental frequency of the AC electric power distribution system is 50 Hz. A harmonic frequency is any sinusoidal frequency, which is a multiple of the fundamental frequency. Harmonic frequencies can be even or odd multiples of the sinusoidal fundamental frequency.
- Flickers: Visual irritation and introduction of many harmonic components in the supply power and their associated ill effects.

Causes of dips, sags and surges:

1. Rural location remote from power source
2. Unbalanced load on a three phase system
3. Switching of heavy loads
4. Long distance from a distribution transformer with interposed loads
5. Unreliable grid systems
6. Equipments not suitable for local supply

ENERGY STORAGE SYSTEMS

Storage systems can be used to protect sensitive production equipments from shutdowns caused by voltage sags or momentary interruptions. These are usually DC storage systems such as UPS, batteries, superconducting magnet energy storage (SMES), storage capacitors or even fly wheels driving DC generators .The output of these devices can be supplied to the system through an inverter on a momentary basis by a fast acting electronic switch. Enough energy is fed to the system to compensate for the energy that would be lost by the voltage sag or interruption. Though there are many different methods to mitigate voltage sags and swells, but the use of a custom Power device is considered to be the most efficient method. For example, Flexible AC Transmission Systems (FACTS) for transmission systems, the term custom power pertains to the use of power electronics controllers in a distribution system, specially, to deal with various power quality problems. Just as FACTS improves the power transfer capabilities and stability margins, custom power makes sure customers get pre-specified quality and reliability of supply. This pre-specified quality may contain a combination of specifications of the following:

- low phase unbalance, no power interruptions, low flicker at the load voltage, low harmonic distortion in load voltage, magnitude and duration of overvoltage and under voltages within specified limits, acceptance of fluctuations, and poor factor loads without significant effect on the terminal voltage There are many types of Custom Power devices.

Some of these devices include:

- Active Power Filters (APF), Battery Energy Storage Systems (BESS), Distribution STATic synchronous COMpensators (DSTATCOM), Distribution Series Capacitors (DSC), Dynamic Voltage Restorer (DVR), Surge Arresters (SA), Super conducting Magnetic Energy Systems (SMES),

- Static Electronic Tap Changers (SETC), Solid-State Transfer Switches (SSTS), Solid State Fault Current Limiter (SSFCL), Static Var Compensator (SVC), Thyristor Switched Capacitors (TSC), and Uninterruptible Power Supplies (UPS).

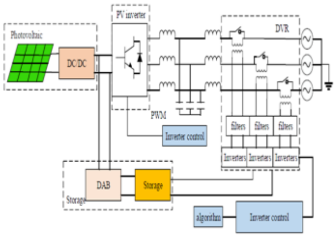


Fig Photovoltaic energy storage and grid connected system structure

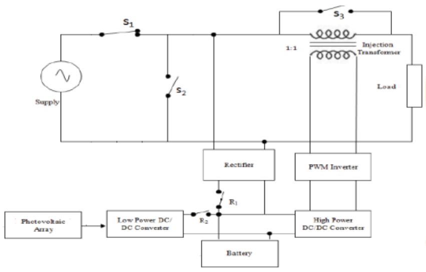


Fig. Block Diagram of the proposed PV based DVR.

CONCLUSIONS FROM THE LITERATURE REVIEW

the analysis of literature review the literature survey a separately excited DVRbased on controlled PVDC link has been designed to mitigate voltage sag and swell, via a correction ratio of up to ± 30%. This is based on assumption that there is a sufficient andconstant DC source in the input of the inverter. Depending on the PI controller, suitable compensating voltage vector via series injection transformer will compensate the ratio of voltage disturbance and the load would not be affected by disturbance at the source. The second survey the ways to speed up the technology development towards the extensive integration of the DVR in the near future. As mentioned above; the DVR can be

integrated into the network in several control configurations to overcome the problems related to power quality. In this work, the DVR is integrated to a power grid connected to a PV farm in order to mitigate the intermittency and variability of solar energy and overcome grid faults caused by voltage sags and swell at the PCC. The proposed DVR control scheme employs a fuzzy logic controller and an in-phase compensation technique. The designed

DVR and the electric system are evaluated under various fault conditions. Third survey organized DVR for preventing customers from momentary voltage disturbance on the utility side and In order to avoid minimize the active power injection into the grid. Voltage sag are result of transient phenomenon in power grid such as short circuit in the upstream power in tx line ,inrush current involved with the starting of large machine, sudden change of load, etc Traditional methods of tap changing transformer and UPS are bulky, costly and not fast enough eliminate the voltage sag load side. Custom power devices

DVR as power electronics based solution to minimize costly outcomes of voltage sag. The fourth literature survey for current-source non-linear loads, the power quality of the output voltage deteriorated slightly. However, for voltage-source loads, the quality of the load voltage was poor. Two alternatives were compared with the SF controller: a PID and a cascade controller. The SF controller was less intuitive, but its design was straightforward. The PID and the cascade controllers exhibit accurate performance; however, when there was no load connected downstream the DVR, only the SF controller was able to properly damp the LC filter resonance. Control alternatives like hysteresis controllers can be found in the literature and a comparative analysis with this alternative is of interest for further research. The fifth literature survey DVR is operated in Standby mode, where the PV array voltage is zero and the inverter is not active in the circuit to keep the voltage to its nominal value. Active mode, where the DVR senses the sag, swell and outage. DVR reacts fast to inject the required single phase compensation voltages. Bypass mode, where DVR is disconnected and bypassed in case of maintenance and repair. Power saver mode, when the PV array with low power dc-dc converter voltage is enough to handle the load. The final literature survey The proposed DVR system model utilizes solar energy stored in the battery for mitigation of voltage sags, swells and interruptions hence result in huge cost saving for consumer side and reduces the payback period of the DVR system. It stores the solar energy in separate back up for night time utilization purpose rather than drawing energy from the grid which minimizes the usage of grid energy and increases the reliability of DVR for the consumer. Further Hardware implementation and testing the efficacy of the proposed system is under the future scope of work.

REALIZATION OF COMPENSATION TECHNIQUE

PHOTOVOLTAIC DESIGN AND MODELING

The PV module is shown in fig , it consists of cells ,based upon the requirements of the consumer may decide the the solar cells charging capability .based upon that only solar cells are manufactured. Number of rows and columns in the solar panel also depends upon the consumers requirement only . It may be a cell voltage at very low or high also depends.

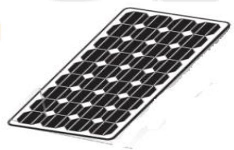


Fig PHOTOVOLTAIC MODULE

MODELING OF PV CELL

The equation of current supplied to the load can be given as.

$$I = I_{PV} - I_O\left[\exp\left(\frac{V + IR_S}{aV_T}\right) - 1\right] - \left(\frac{V + IR_S}{R_P}\right)$$

Where IPV–Photocurrent current, IO–diode’s Reverse saturation current, V–Voltage across the diode, a– Ideality factor VT –Thermal voltage Rs– Series resistance Rp–Shunt resistance

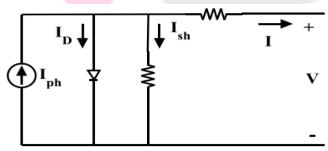


Fig Equivalent circuit of Single diode modal of a solar cell

PV cell photocurrent, which depends on the radiation and temperature, can be expressed as.

$$I_{PV} = (I_{PV\_STC} + K_I \Delta T)\frac{G}{G_{STC}}$$

Where,

KI– cell’s short circuit current temperature coefficient

G–solar irradiation in W/m2

GSTC–nominal solar irradiation in W/m2

IPV_STC– Light generated current under standard test condition.

Efficiency of a PV cell does not depend on the variation in the shunt resistance. Rp of the cell but efficiency of a PV cell greatly depends on the variation in series resistance Rs. As Rp of the cell is inversely proportional to the shunt leakage current to ground so it can be assumed to be very large value for a very small leakage current to ground.

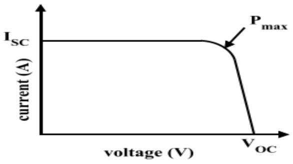


Fig. IV characteristics

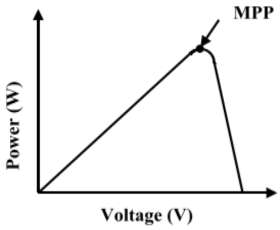


Fig. PV characteristics

MPPT CONTROL TECHNIQUE

Maximum power point tracing (MPPT) system is an electronic control system that can be able to coerce the maximum power from a PV system. It does not involve a single mechanical component that results in the movement of the modules changing their direction and make them face straight towards the sun. MPPT control system is a completely electronic system which can deliver maximum allowable power by varying the operating point of the modules electrically.

NECESSITY OF MAXIMUM POWER POINT TRACKING

In the Power Vs Voltage characteristic of a PV module shown in fig 2.8 we can observe that there exist single maxima i.e. a maximum power point associated with a specific voltage and current that are supplied. The overall efficiency of a module is very low around 12%. So it is necessary to conditions. This increased power makes it better for the use of the solar PV module. A DC/DC converter which is placed next to the PV module extracts maximum power by matching the impedance of the circuit to the impedance of the PV module and transfers it to the load. Impedance matching can be done by varying the duty cycle of the switching elements. MPPT algorithm There are many algorithms which help in tracing the maximum power point of the PV module. They are following: a. P&O algorithm b. IC algorithm c. Parasitic capacitance d. Voltage based peak power tracking e. Current Based peak power tracking Perturb and observe Each and every MPPT algorithm has its own advantages and disadvantages. Perturb and observe (P&O) method is widely used due its simplicity. In this algorithm we introduce a perturbation in the operating voltage of the panel. Perturbation in voltage can be done by altering the value of duty-cycle of dc-dc converter operate it at the crest power point so that the maximum power can be provided to the load irrespective of continuously changing environmental

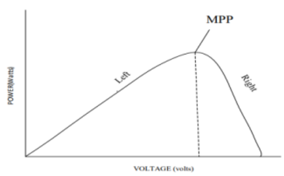


Fig P-V characteristics (basic idea of P&O algorithm)

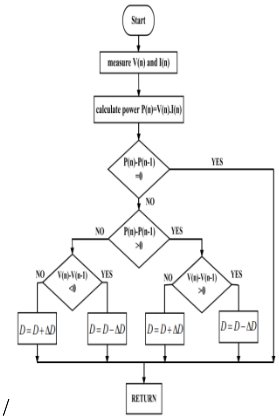


Fig Flowchart of Perturb & Observe MPPT algorithm

DVR CONTROLLER- DQ TRANSFORMATION

A 25kV, 100 MVA short-circuit level, equivalent network feeds a 5 MW, 5 Mvar capacitive load. The internal voltage of the source is controlled by the Discrete 3-Phase Programmable Voltage Source block. The programmable source dialog box and look at the parameters controlling the voltage and frequency. A 60 Hz, positive-sequence of 1.0 pu, 45 degrees is specified. At t = 0.5 s, a sinusoidal modulation of the frequency (amplitude 3 Hz, frequency 0.4 Hz) is started. The modulation stops at t =3 s so that a full cycle of modulation can be observed. The Three-Phase V-I Measurement block is used to monitor the three load voltages and currents. Open its dialog box and see how this block allows to output the three voltages and currents in p.u.The Discrete 3-Phase PLL block measures the frequency and generates a signal (wt output) locked on the variable frequency system voltage. The PLL drives two measurement blocks taking into account the variable frequency: one block computing the fundamental value of the positive-sequence load voltage and another one computing the load active and reactive powers. These two blocks and the PLL are initialized in order to start in steady state. The whole system, (power network, PLL and measurement blocks) is discredited at a 50 us sample time.

THREE PHASE PLL

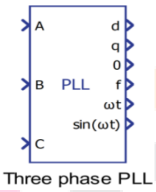


Fig Three Phase PLL

The synchronous frame three phase PLL is widely used for tracking grid voltages and currents and for providing a synchronization signal to inverter based distributed resources. A conceptual block diagram of the synchronous frame three phase PLL is shown in

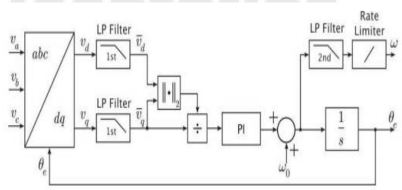


Figure .Synchronous frame three-phase PLL block diagram.

The abc-to-dq transformation makes use of the dq rotating reference frame in which the d-axis leads the q-axis. The end result is alignment of the d-axis with the peak of phase A in a balanced set (positive rotation). The low pass filters remove noise and oscillations from the d and q-axes measurements. The goal of the PI controller is to track the frequency, and phase of the three-phase input signal by forcing the q-axis component to zero through continuously adjusting the oscillator frequency (embodied by the integrator). An automatic gain controller estimates the amplitude of the input voltage which is then used to normalize the q-axis voltage.

Normalizing the input signal improves the PLL controller bandwidth by enabling it to track input signals with a wide range of amplitude variations.

A rate limiter and second order low pass filter are used to filter out noise and oscillations from the frequency measurement (in Hz).

PORTS

A (in)

Input A related to the three-phase system whose phase and frequency is intended to be extracted.

- Supported types: real.
- Vector support: no.

B (in)

Input B related to the three-phase system whose phase and frequency is intended to be extracted.

- Supported types: real.
- Vector support: no.

C (in)

Input C related to the three-phase system whose phase and frequency is intended to be extracted.

- Supported types: real.
- Vector support: no.

d (out)

Output signal of the component related to the direct component of the abc-frame input.

- Supported types: real.
- Vector support: no.

q (out)

Output signal of the component related to the quadrature component of the abc-frame input.

- Supported types: real.
- Vector support: no.

0 (out)

Output signal of the component related to the zero component of the abc-frame input.

- Supported types: real.
- Vector support: no.

This output is dynamically created when property enable 0 axis output (from dq0) is set to True

f/ω (out)

Frequency of the three-phase input system. This value can be output as Hz or rad/s, depending on the value set on the component' parameter frequency output unit.

- Supported types: real.
- Vector support: no.

ωt (out)

Angle of the three-phase input system.

- Supported types: real.
- Vector support: no.

sin(ωt) (out)

Value of the trigonometric function 'sin' applied to the value of the phase of the three-phase system.

- Supported types: real.
- Vector support: no.
- This output is dynamically created when property enable sin(ωt) output is set to True

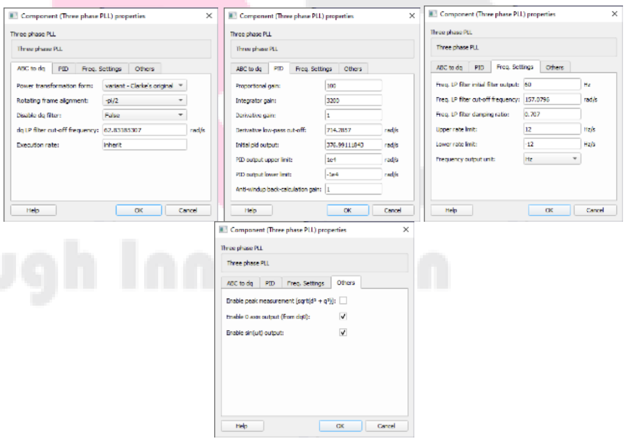
peak (out)

Peak value of the dq transformation system. It is given by the following equation:

$$peak = \sqrt{d^2 + q^2}$$

- Supported types: real.
- Vector support: no.
- This output is dynamically created when property enable peak measurement [sqrt(d^2+q^2)] is set to True

PROPERTIES



Power transformation form

Choose between the methods to perform the transformation. The methods available are:

Variant – Clarke's original: Use this method when you want the resulting dq0 rotating frame to be amplitude invariant. That is, the amplitude of the original three-phase system will be preserved in the dq0 rotating frame.

Variant – uniform: Use this method when the input 'abc frame' signal is a balanced system and you want the resulting dq0 rotating frame to be amplitude invariant. That is, the amplitude of the original three-phase system will be preserved in the dq0 rotating frame.

Invariant: Use this method when you want the resulting dq0 rotating frame to be power invariant. That is, the power of the original three-phase system will be preserved in the dq0 rotating frame.

- **Rotating frame alignment**
  - Chose to define the alignment of the dq signals ($-\pi/2$ = "q", 0 = "d").
- **Disable dq filter**
  - Chose to enable or disable a low passive filter on the signals d and q.
- **dq LP filter cut-off frequency**
  - Type in the cutoff frequency of the dq filter in rad/s. This parameter only is available when the property "Disable dq filter" is set to False.
- **Proportional gain**
  - Type in the gain value to be applied to the proportional gain of the PID control block.
- **Integrator gain**
  - Type in the gain value to be applied to the integrator gain of the PID control block.

Derivative gain

Type in the gain value to be applied to the differentiator gain of the PID control block.

Derivative low-pass cut-off

Type in the derivative low-pass filter time constant that is used to implement the derivative action since it is not possible to implement a transfer function like $K_d \cdot s$. The implementation of a derivative action, therefore, is done as in:

$$u_d = K_d \frac{N \cdot s}{s + N}$$

Hence, if N is sufficiently large, u_d tends to the ideal implementation of a derivative action $K_d \cdot s$.

Initial pid output

Type in the initial condition of PID control action.

PID output upper limit

Type in the upper limit of the output signal of the PID block.

PID output lower limit

Type in the lower limit of the output signal of the PID block.

Anti-windup back calculation gain

Type in the value of the gain of the anti-windup action of the integral parcel of PID controller.

Freq. LP filter initial filter output

Type in the initial value of the low-pass filter for the output frequency.

Freq. LP filter cut-off frequency

Type in the cutoff frequency of the low-pass filter for the output frequency.

Freq. LP filter damping ratio

Type in the damping ratio of the second-order low-pass filter for the output frequency.

Upper rate limit

Type in the upper rate limit for the frequency signal that is output by the PID controller and is the input of the second-order low-pass filter.

Lower rate limit

Type in the lower rate limit for the frequency signal that is output by the PID controller and is the input of the second-order low-pass filter.

Frequency output unit

Choose which frequency unit will be output by the component: Hz or Rad/s.

Enable peak measurement [sqrt(d^2+q^2)]

Select it to create an output for the component which will output the peak measurement of the three-phase input signal.

Enable 0 axis output (from dq0)

Select it to create an output for the component which will output the zero component of the abc-frame input.

Enable sin(ωt) output

Select it to create an output for the component which will output the value of the trigonometric function 'sin' applied to the value of the phase of the three-phase system.

Execution rate

Type in the desired signal processing execution rate. This value must be compatible with other signal processing components of the same circuit: the value must be a multiple of the fastest execution rate in the circuit. There can be up to four different execution rates, but they must all be multiple of the basic simulation timestep. To specify the execution rate, you can use either decimal (e.g. 0.001) or exponential values (e.g. 1e-3) in seconds. Alternatively, you can type in 'inherit' in which case the component will be assigned execution rate based on the execution rate of the components it is receiving input from.

PI CONTROLLER

The Discrete PI Controller block performs discrete-time PI controller computation using the error signal and proportional and integral gain inputs. The error signal is the difference between the reference signal and the measured feedback. The block outputs a weighted sum of the input error signal and the integral of the input error signal. Can tune the Discrete PI Controller coefficients (*Kp* and *Ki*) either manually or automatically. Automatic tuning requires Simulink® Control Design™ software.

PORTS
Input

- error — Variation of system output from expected value **scalar.** Difference between a reference signal and the system output.
  - K_p — Proportional gain scalar
- K_i*T_s — Integral gain pre-multiplied by integrator sample time **scalar**

Output

- y — PI controller output **scalar**

DISCRETE PWM-BASED CONTROL SCHEME

In order to mitigate the simulated voltage sags in the test system of each compensation technique, also to compensate voltage sags in practical application, a discrete PWM-based control scheme is implemented, with reference to DVR. The aim of the control scheme is to maintain a constant voltage magnitude at the sensitive load point, under the system disturbance. The control system only measures the rms voltage at load point, for example, no reactive power measurement is required. Figure shows the DVR controller scheme implemented in MATLAB/SIMULINK. The DVR control system exerts a voltage angle control as follows: an error signal is obtained by comparing the reference voltage with the rms voltage measured at the load point. The PI controller processes error signal and generates the required angle δ to drive the error to zero, for example; the load rms voltage is brought back to the reference voltage.

It should be noted that, an assumption of balanced network and operating conditions are made. The modulating angle δ or delta is applied to the PWM generators in phase A, whereas the angles for phase B and C are shifted by 240° or -120° and 120° respectively.

$$V_A = Sin\ (\omega t + \delta)$$

$$V_B = Sin\ (\omega t + \delta - 2\pi/3)$$

$$V_C = Sin\ (\omega t + \delta + 2\pi/3)$$

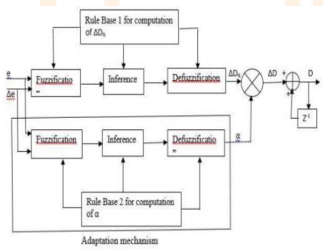


Fig AFLC control structure

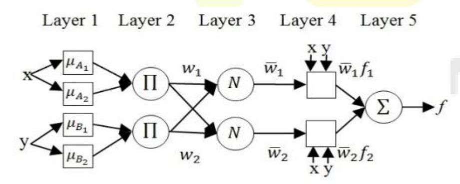


Fig ANFIS architecture

Table Two-Pass ANFIS Learning Algorithm

	Forward Pass	Backward Pass
Antecedent Parameters	Fixed	GD
Consequent Parameters	LSE	Fixed
Signals	Node Outputs	Error Signals

functions of gaussian shape with two parameters center (c) and width (σ).

Layer 2 calculates the firing strength of a rule via product operation.

Layer 3 is normalized firing strength of a rule from previous layer.

In Layer 4, each node represents consequent part of fuzzy rule. The linear coefficients of rule consequent are trainable.

Nodes in Layer 5 perform defuzzification of consequent part of rules by summing outputs of all the rules. Further detail on computation performed in ANFIS can be found in the related research.

The two pass learning algorithm tunes consequent parameters by LSE in forward pass and while back-propagating error back to first layer, it updates membership functions using GD. Since, GD is influenced by back propagation (BP) algorithm of ANN, which has the drawback to be likely trapped in local minima [8]. On the other hand, the convergence of gradient method is also very slow; depending on initial parameter values.

CONCLUSION

This paper has presented the power quality problems such as voltage dips, swells, distortions and harmonics. Compensation techniques of custom power electronic devices DVR was presented. The design and applications of DVR for voltage sags and comprehensive results were presented. A PWM-based control scheme was implemented. This characteristic makes it ideally suitable for low-voltage custom power applications. Design and implementation of solar energy-based three phase DVR, using fuzzy logic controlled novel boost inverter, for mitigation of deep voltage sags, swells and an interruption affecting the sensitive equipment connected on low voltage distribution side, has been proposed. The proposed DVR system model utilizes solar energy stored in the battery for mitigation of voltage sags, swells and interruptions hence result in huge cost saving for consumer side and reduces the payback period of the DVR system. It stores the solar energy in separate back up for night time utilization purpose rather than drawing energy from the grid which minimizes the usage of grid energy and increases the reliability of DVR for the consumer. Further fuzzy logic controlled novel boost inverter improves overall efficiency and dynamic performance of the DVR system.

References

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