

Design and Simulation of Micro strip Antenna Array For 5G Applications

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Abstract: A research paper presents a 5G communication application, specifically a high gain linear 1*2, 1*4 antenna array that utilizes a circular patch. The designed antenna operates at a frequency of 28 GHz and employs the micro strip feed technique for feeding the array. The antenna is constructed on a Rogers RT/Duroid 5880 substrate with a thickness of 0.254 mm and a dielectric constant of 2.2. The paper also includes a comprehensive electromagnetic analysis of the antenna using the commercially available HFSS software.

Keywords—5G, micro-strip patch antenna, return loss, Ansoft HFSS

Introduction:

As the demand for faster data speeds and greater bandwidth continues to rise, researchers and scientists are actively exploring advancements in mobile communication to meet the needs of future generations. The limitations of 4G (Long Term Evolution) technology have prompted these efforts, leading to the development of the fifth generation (5G) of mobile networks. 4G offered impressive features such as data rates ranging from 100 to 300 megabits per second, ample bandwidth, improved connectivity compared to 3G, and enhanced network security, all at affordable prices. However, one significant aspect that 4G failed to fully deliver on was reliable connectivity. Due to the limitations of 4G, consumers were compelled to switch to expensive broadband connections, making them inaccessible for many individuals.

To accommodate a broad range of applications in the millimetre wave frequency, various frequency bands ranging from 24 to 86 GHz have been allocated for 5G deployment. The millimetre-wave band offers significant advantages, including the ability to support diverse applications and enable antenna miniaturization. his new technology offers data rates three times faster than the previous generation. Xiaomi, a mobile company, is actively developing phones that are compatible with both 4G and 5G spectrums. One of the key aspects of the fifth generation is the Internet of Things (IoT), where every device will be able to synchronize with each other. With 5G, data rates of at least 1 Gigabit per second will be possible, enabling seamless connectivity for electronic gadgets.

The upcoming generation of communication technology holds tremendous potential for diverse applications, including industry, healthcare, and more. Realizing these possibilities relies on 5G's ability to provide large bandwidth, low power consumption, high data rates, and latency less than 1ms. Antennas serve as the fundamental component for implementing the new generation of mobile technology. These antennas should provide high speed, enhanced bandwidth, and significant gain. The selection of suitable materials for antenna design, tailored to specific applications, is crucial. Various dielectric materials such as Rogers RT Duroid, FR-4, and Polyamide are available for this purpose. Microstrip Antennas (MSA) are particularly advantageous due to their low profile. As 5G requires high gain, the development of an array consisting of single-element antennas becomes

necessary. circular antenna design by introducing circular slots to excite higher modes, and an inset feed is implemented to improve impedance matching. Furthermore, to achieve high gain, we present a linear array of slotted circular patch antennas designed for a center frequency of 28 GHz.

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ANTENNA DESIGN:

1. Design single patch antenna

In the initial stage, a single radiating patch is designed to function at a resonant frequency of 28GHz. The design of the patch antenna comprises multiple components, including the substrate, radiating patch, feed line, and inset feed gap. The subsequent section provides the design equations and calculations associated with these components.

2. Substrate:

In order to minimize dielectric loss, it is important to select a substrate material with a very low loss tangent. Additionally, using a thinner substrate helps to reduce spurious radiations and suppress higher modes of

operation, thereby minimizing radiation losses. To meet these requirements, the substrate RT/duroid5880 is chosen, which has a relative permittivity (ϵ r) of 2.2 and a loss tangent (tan δ) of 0.0009.

3. Widtha of the antenna:

The width (W) of the radiating patch plays a crucial role as it influences the directivity and radiation resistance, thereby impacting the bandwidth and efficiency of the patch antenna. Therefore, it is important to select an optimal value for 'W'. The equation for determining the width of the microstrip patch antenna is provided below.

$$w \le \frac{c}{2fr} \left(\frac{\epsilon+1}{2}\right) - \frac{1}{2}$$

In the given equation, c represents the speed of light $(3x10^8 \text{ m/s})$, and fr is the resonant frequency at which the antenna is designed to operate. For this specific design, the resonant frequency fr is set at 28 GHz. Applying the equation, the resulting width of the patch is determined to be mm.

4. Length of the patch:

The length 'L' of the radiating patch is determined by the effective dielectric constant (ϵ eff) of the substrate and the resonant frequency (fr).

$$\mathcal{E}_{e=(\frac{\varepsilon r+1}{2})+(\frac{\varepsilon r-1}{2})(1+\frac{12h}{w})-\frac{1}{2}}$$
$$L=\frac{c}{2fr\sqrt{\varepsilon e}}-2\Delta L$$

The effective dielectric constant (ϵ eff) is determined using the above equation. Subsequently, the effective length 'L' is calculated using the above equation, which includes the term accounting for the fringing effect (2 δ), computed using the above equation.

5. Micro strip line feed

A commonly used method involves feeding the microstrip antenna through a microstrip line, which is connected to the antenna at the center of one of its edges. The microstrip line can be linked to a feeding circuit or directly connected to a signal source, spanning across the microstrip line and the ground plane.

Single antenna design:

Fig 1:single antenna



Depicted in Fig1 . The antenna model utilizes a circular patch with dimensions of 6 mm (Lp) \times 5.8 mm (Wp). The patch is constructed using RT Duroid 5880 substrate material with a relative permittivity of 2.2. The overall height of the antenna, including the substrate thickness, is 1.27 mm (h). The design is simulated using HFSS software.

 1×2 array antenna design:



The formula for determining the characteristic impedance (Z0) of a Quarter Wave Transformer is as follows: $Z0 = \sqrt{(ZL * Zin)}$, where Z0 represents the characteristic impedance in ohms, ZL denotes the load impedance in ohms, and Zin represents the input impedance.

In the provided figure, an antenna array model is presented, featuring a circular patch with dimensions of 6 mm (length, Lp) \times 11 mm (width, Wp). The patch is constructed using RT Duroid 5880 substrate material with a relative permittivity of 2.2. The antenna's overall height, inclusive of the substrate thickness, measures 0.787 mm (h). HFSS software is utilized for simulating the design.

Proposed method:

1×4 array antenna



The array comprises four elements and has an overall size of 9 mm \times 25 mm \times 1.27 mm (L2 \times W2 \times h) The antenna design is specifically tailored for one of the frequency bands that may be utilized in future 5G mobile communication.

SIMULATION AND RESULT:

The simulation software Ansoft HFSS, also known as High Frequency Structure Simulator, is employed to model and analyze antennas. In this study, a single patch antenna, 1×2 and a 1x4 array of antennas are simulated to assess their respective performances and make comparisons.

Performance of the single patch antenna:

The S11 plot in Figure 4 demonstrates a return loss value of -21.93 dB at 28 GHz, while Figure 5 depicts the VSWR plot of the designed single patch antenna, which exhibits a value of 1.17 at the same frequency of 28 GHz.

The 3D radiation pattern of the single patch antenna is visualized in Figure 6 demonstrating a single balloon shape with the main-lobe peak aligned along the Z-axis. At 28 GHz, the antenna exhibits a gain of 9 dB. To optimize the design and improve the gain, minimize return loss, and ensure proper impedance matching, an array configuration is implemented.





Performance of the 1×2 array antenna:

Figure 7 presents the S11 (return loss) versus frequency plot for the 1x2 array antenna, revealing a resonant frequency of 28 GHz with a value of -14.49 dB. Figure 8 complements this by showing the VSWR versus frequency plot, indicating a value of 1.15 at 28 GHz. This VSWR value is within the desired range of being less than 2, signifying the antenna's optimal performance.

Figure 9 illustrates the 3D radiation pattern of the single patch antenna, showcasing a distinctive balloon shape with the main lobe's peak aligned along the Z-axis. At 28 GHz frequency, the antenna achieves a gain of 11 dB.



Performance of the 1×4 array antenna :

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Figure 10 displays the S11 (return loss) versus frequency plot of the 1x4 array antenna, indicating a value of -20.49 dB at the resonant frequency of 28 GHz. Furthermore, Figure 11 illustrates the VSWR versus frequency plot of the antenna, with a value of 1.19 at 28 GHz, which is within the desired range of being less than 2, making it an optimal result.

The 3D radiation pattern of the single patch antenna is visualized in Figure 12, demonstrating a single balloon shape with the main-lobe peak aligned along the Z-axis. At 28 GHz, the antenna exhibits a gain of 13dB.

Fig 10: S_{11} plot of the array antenna



A comparison of the result parameters between the single patch antenna, 1×2 and 1×4 array antenna reveals notable improvements in terms of s_{11} , VSWR and gain parameters in the array configuration. These enhancements are observed at the resonant frequency of 28 GHz when compared to the single patch antenna.

Table: comparison of the results

S.N	parameters	Single antenna	1×2 antenna	1×4 antenna
			array	array
			2	2
1	S11	-20.49dB	-14.49	-21.49dB
2	VSWR	1.17	1.15	1.19
3	gain	9dB	11dB	13dB

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CONCLUSION:

A proposed circular patch antenna design, operating at 28 GHz, incorporates a single element with a 50- Ω microstrip feed line. The substrate material, Rogers RT/Duroid 5880, is chosen for its suitability in the Ka-band region. The main element exhibits a return loss of approximately -20.49 dB and a gain of 9 dB. Furthermore, a compact linear array antenna is constructed by combining two and four of these single element antennas using a series fed network. The array has overall dimensions of $9 \times 25 \times 1.274$ mm³. By employing a center series fed technique, in line with traditional array configuration methods, a maximum gain of 11 and 13 dB is achieved while maintaining a reflection coefficient below -10 dB. The measured and simulated results show excellent agreement, underscoring the antenna's viability for 5G applications.

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